

# Kisaco Innovation Radar on Secure and Private Compute 2021

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Motivation, Contents Table, list of Figures,  
and example content

## Motivation

The cybersecurity landscape is undergoing radical transformation through breakthrough technologies that for the first time offer secure and private compute (SPC) to enterprises. We wish to encrypt data across the three phases that track its complete lifecycle: at rest, in flight, and during compute. With encryption of data throughout its lifecycle and with the encryption keys held by and never leaving the data owner, it is possible to offer maximum security and privacy. The challenge has been creating viable encryption in compute schemes – this is what SPC technologies tackle. The usual, current practice in any computation on encrypted data is that first you need to decrypt it, perform the required computation, and then encrypt the results. The challenge and ‘holy grail’ has been to compute with encrypted data and do so within reasonable time and cost.

SPC has emerged in recent years with a number of competing technologies which each have their strengths and weaknesses. This report explains the technology and market landscape and profiles some of the new leading players pioneering this field. We cover technologies such as homomorphic encryption (HE) and its various modes, including fully homomorphic encryption (FHE), as well as enclaves in trusted execution environments (TEE) and applications in secure multi-party computation (MPC). We also name the key organizations that are driving this community, describe the efforts in standardization, and identify business use cases that enterprises should be aware and learn about today. The report further provides profiles on vendors at the cutting edge of SPC: Cornami, Cybernetica, Decentriq, Duality, Inpher, and Zama.

## What you will learn:

- How is secure and private compute (SPC) different from other forms of data encryption and what will its impact be on businesses and how we use the internet and cloud.
- What are the key technologies enabling SPC, including an introduction to FHE, TEE and MPC.
- The strengths and weaknesses of the key enabling technologies.
- What is on the roadmap in SPC and what will be the impact of hardware accelerators.
- Which are the key SPC vendor associations and what is their activities.
- We provide in-depth profiles on each participating vendor, who are pioneers in SPC.

## Contents

Kisaco Research View .....	3
Motivation .....	3
Key findings .....	3
SPC technology and market landscape .....	4
Introduction to SPC .....	4
Naming conventions and associations .....	4
What is new and possible today that is different from the past .....	4

The business use case for SPC .....	5
The key technologies in SPC .....	6
Lattice-based cryptography .....	6
Fully Homomorphic Encryption (FHE) .....	6
Somewhat and Partial Homomorphic Encryption (SHE, PHE) .....	8
Enclaves in TEE .....	9
Quantum proofing SPC .....	10
Comparing the SPC cryptographic schemes .....	10
Hardware acceleration for SPC .....	14
The DARPA DPRIVE initiative .....	14
Acceleration with GPUs, FPGAs, and domain specific architectures .....	15
Vendor analysis .....	15
Cornami .....	16
Cybernetica .....	19
Decentriq .....	22
Duality Technologies .....	24
Inpher .....	27
Zama .....	30
Appendix .....	33
The SPC industry associations .....	33
Confidential Computing Consortium .....	33
Homomorphic Encryption Standardization .....	34
MPC Alliance .....	35
Acknowledgements .....	35
Author .....	35
Kisaco Research Analysis Network .....	36
Copyright notice and disclaimer .....	36

## Figures (including tables)

Figure 1: Securing the data lifecycle: the problem without encrypting compute.

Figure 2: Lattice-based cryptography.

Figure 3: HE operations of addition and multiplication on data items.

Figure 4: FHE breakthrough: bootstrapping.

Figure 5: TEE.

Figure 6: Comparing HE, TEE, and secure sharing MPC

Figure 7: FHE compute time using LAWS

Figure 8: The vendors profiled in this report and key technologies they offer.

Figure 9: Comparing a simple 1D convolution operation on a CPU, GPU, and Cornami chip.

Figure 10: The Cornami software stack.

Figure 11: Configuring the hardware to the “shape of the software”.

Figure 12: Sharemind: building services with end-to-end data protection.

Figure 13: Comparing the two versions of Sharemind.

Figure 14: The Decentriq platform: high-level workflow and components for the MPC example.

Figure 15: Duality products and application areas.

Figure 16: Inpher entry in the IDASH Privacy and Security Workshop 2020.

Figure 17: Inpher XOR solution.

Figure 18: The Zama software stack for FHE.

Figure 19: Venn diagram of various technologies used to protect data in use.

Figure 20: MPC concerns two or more parties wishing to share information confidentially.

## Example Content

# Introduction to SPC

## Naming conventions and associations

SPC is our choice of label for cryptographic technology applied to the compute part of the data lifecycle. However, cryptographic communities are using labels such as privacy-preserving computing, and data oblivious computing. Furthermore, three associations have formed which also embrace certain choices of terminology, and we identify these as follows:

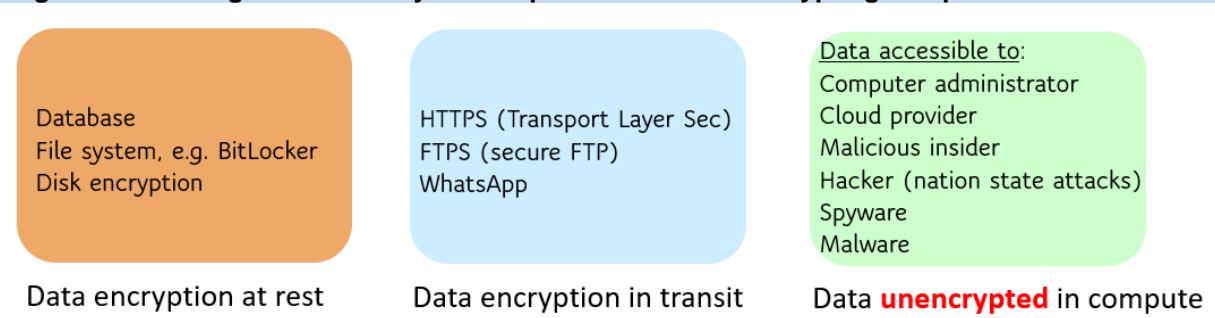
- **Confidential Computing Consortium:** This vendor consortium uses the preferred term “confidential computing” to refer to TEE available on CPU hardware.
- **Homomorphic Encryption Standardization:** This organization spans vendors, government and academia and, as the name implies, is concerned with HE.
- **MPC Alliance:** This is a vendor consortium that is concerned with MPC. MPC is a task or requirement and needs to be specified with the cryptographic technology being applied. In many cases the choice is secret sharing, and it appears there is reluctance with some vendors that use HE or TEE to use the term MPC when the use case is MPC. In this report we qualify the label MPC with the technology used.

The appendix provides further details on these vendor and community associations.

## What is new and possible today that is different from the past

Thinking about the journey that data takes in typical scenarios: it is generated and protected in storage by encryption; it is transferred across networks between computers such as sending to the cloud and protected during transit by encryption. Traditionally the data is then decrypted in order that computation can be performed on the data, such as feeding it to a neural network for training or performing some statistical analysis on the data. What SPC offers is protection of the data during computation. This means there is no access to the data at any point during its compute without protection, the only people able to retrieve the plaintext (the readable message or data) are the owners of the encryption keys. This applies even to staff administering the computers in which the computation occurs – traditionally administrators could observe private data if they wished and with SPC this is not possible. To summarize what is new: data can now be protected throughout its lifecycle: during storage, in transit, and the new part, during compute – see Figure 1.

**Figure 1: Securing the data lifecycle: the problem without encrypting compute**



Source: Kisaco Research

There are several SPC technologies in play today and new ones are emerging from academia, a new generation of startups are emerging to bring these technologies into general use across enterprises and even consumers. We witness how secure hypertext transfer protocol (https) is now a default in the web over the insecure http standard, similarly we expect SPC technologies to become ubiquitous and in some applications to be compulsory. This will happen as standards are created and adopted, and as awareness and education of SPC grows.

## Vendor analysis

Kisaco Research received briefings from leading players in the SPC space. The participating vendors are listed in Figure 8 and are profiled in this section. The table summaries key technologies available from the vendor and the FHE libraries used where relevant. All the vendors' primary business is offering software cryptographic solutions except Cornami which is developing a FHE chip accelerator. The software vendors are also partnering with hardware accelerator providers of GPU, FPGA, and ASIC.

**Figure 8: The vendors profiled in this report and key technologies they offer**

Vendor	FHE	FHE library	MPC	TEE
<b>Cornami</b> (acceleration chip)	✓	Any		
<b>Cybernetica</b>			✓	
<b>Decentriq</b>				✓
<b>Duality</b>	✓	Palisade		
<b>Inpher</b>	✓	TFHE	✓	
<b>Zama</b>	✓	Concrete (TFHE)		

Source: Kisaco Research

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